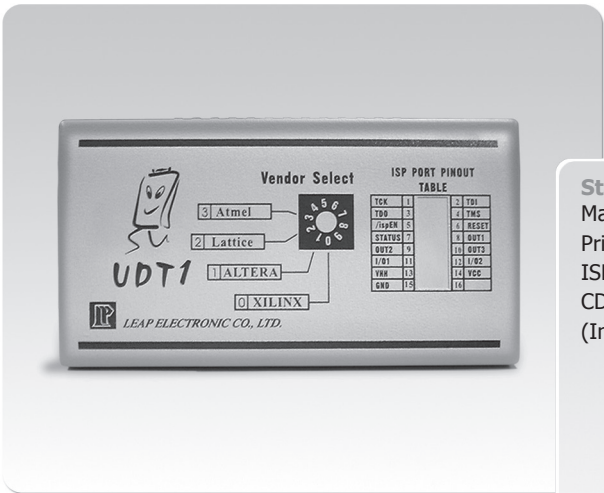


UDT-1

Universal JTAG/ISP Programmer

Introduction

PLD (Programming Logic Device) is the most common and easy used logic device. It has lots of advantages, such as high performance, low cost, flexible design and easy field configuration or customization. Each CPLD supplier has its own "download kit" so we develop UDT-1, the universal IN SYSTEM PROGRAMMER. Users can use UDT-1 to program different branded CPLD / FPGA devices.



Standard Accessories

- Main unit.....x1
- Printer cable.....x1
- ISP cable.....x1
- CD.....x1
(Including driver)

Features

- Support JTAG and ISP specification, on board program function for CPLD and FPGA devices.
- Use vendor's developing system to process design, compile, simulate, debug, on board program and data download.
- Support JTAG /ISP function of FPGA and CPLD manufactured by ALTERA, Atmel, Lattice, XILINX, etc.
- Excellent circuit structure, best programming quality, high yield rate and protection design to give the best service for users' valuable device.
- Use Printer port to connect with PC & Notebook, easy operating.
- Light, thin, short, tiny and portable.

Specification

Input / Output	Parallel Port (Printer Port)
Power	DC 12V/500mA
Operating voltage	+1.8V~+6.0V
Output programming voltage	V _{hh} +12.0V 200mA
Dimension	150 x 80 x 30mm
Weight	20g
Operating Humidity	90% (non-condensing)
Operating Temperature	+5°C ~ +45°C

PC System Requirement

Operating System	Same as the PC system requirments and parallel port setting when installing IC vendor's design software.
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Other Specifications

- 6 standard JTAG and ISP Cables.
- Selective switch for different vendors.
- Red Power LED.