

ADVANCED LOGIC CIRCUIT PRACTICE

By Ou, Chien-Ming, Date: 27 August 2003

Preface (1)

Chapter 1 Basic Logic Gate Practice (20)

1-1 Practice purpose

1-2 Practice theory

1-2-1 Logic Function

1-2-2 Logic Gate

1-3 Practice item

1-3-1 NOT gate circuit

1-3-2 OR gate circuit

1-3-3 AND gate circuit

1-3-4 NOR gate circuit

1-3-5 NAND gate circuit

1-3-6 XOR gate circuit

1-3-7 3 INPUT AND gate circuit

1-4 Questions & Discussion

Chapter 2 Combinational Logic Circuit Practice (19)

2-1 Practice purpose

2-2 Practice theory

2-2-1 NAND & NOR Logic analysis

2-2-2 Karnaugh map

2-2-3 [Table method](#)

2-3 Practice items

2-3-1 AND-OR gate circuit

2-3-2 NAND-NAND gate circuit

2-3-3 NOR-NOR gate circuit

2-3-4 Duality circuit (NAND gate)

2-3-5 Duality circuit (NOR gate)

2-4 Questions & Discussion

Chapter 3 Arithmetic Circuit Practice (18)

3-1 Practice purpose

3-2 Practice theory

3-2-1 The design of adder

3-2-2 Subtractor

3-2-3 BCD adder

3-3 Practice items

3-3-1 Half-Adder circuit

3-3-2 Full-Adder circuit

3-3-3 Half-Subtractor circuit

3-3-4 Full- Subtractor circuit

3-3-5 4 bits BCD add/sub circuit

3-3-6 BCD add/sub circuit

3-4 Questions & Discussion

Chapter 4 Encoder & Decoder Practice (22)

4-1 Practice purpose

4-2 Practice theory

4-2-1 Decoder

4-2-2 Encoder

4-2-3 The design of 7-segdisplay decoder

4-3 Practice items

4-3-1 2 to 4 Decoder

4-3-2 C-A 7-segdisplay

4-3-3 4 to 2 Encoder

4-3-4 4 to 2 Priority encoder

4-3-5 74138 3 to 8 Decoder

4-4 Questions & Discussion

Chapter 5 Multiplexer & Demultiplexer

5-1 Practice purpose

5-2 Practice theory

5-2-1 Multiplexer

5-2-2 Demultiplexer

5-3 Practice items

- 5-3-1 2 to 1 Multiplexer
- 5-3-2 4 to 1 Multiplexer
- 5-3-3 1 to 4 Demultiplexer
- 5-4 Questions & Discussion

Chapter 6 Flip-flop Practice

- 6-1 Practice purpose
- 6-2 Practice theory
 - 6-2-1 SR Flip-flop
 - 6-2-2 JK Flip-flop, T Flip-flop
 - 6-2-3 D Flip-flop
- 6-3 Practice items
 - 6-3-1 R-S Flip-flop
 - 6-3-2 J-K Flip-flop
 - 6-3-3 T Flip-flop
 - 6-3-4 D Flip-flop
- 6-4 Questions & Discussion

Chapter 7 Counter Practice (16)

- 7-1 Practice purpose
- 7-2 Practice theory
 - 7-2-1 [Asynchronous](#) Counter
 - 7-2-2 Ripple counter
 - 7-2-3 [Asynchronous](#) up/down Counter
- 7-3 Practice items
 - 7-3-1 4 bits UP [Asynchronous](#) Counter
 - 7-3-2 4 bits DOWN [Asynchronous](#) Counter
 - 7-3-3 0 to 9 Counters
 - 7-3-4 0 to 59 Counters
 - 7-3-5 BCD UP/DOWN Counter
 - 7-3-6 JOHNSON Counter
- 7-4 Questions & Discussion

Chapter 8 Shift Register Practice (11)

8-1 Practice purpose

8-2 Practice theory

8-2-1 Serial in Serial Out: SISO

8-2-2 Serial In-parallel Out: SIPO

8-2-3 Parallel in Serial Out: PISO

8-2-4 Parallel in Parallel Out: PIPO

8-3 Practice items

8-3-1 4 bits SIPO shift Register

8-3-2 8 bits SIPO shift Register

8-3-3 8 bits PISO shift Register

8-4 Questions & Discussion

Chapter 9 Multivibrator Circuit Practice (18)

9-1 Practice Purpose

9-2 Practice theory

9-2-1 555 Time setter

9-2-2 Monostable Vibrators

9-2-3 Nonstable Vibrators

9-2-4 The usage of 555 Time setter

9-3 Practice items

9-3-1 Nonstable Multivibrators

9-3-2 555 Timers

9-3-3 Monostable Multivibrators

9-3-4 Multistable Multivibrators

9-4 Questions & Discussion

Chapter 10 Appliance of Circuit Practice (4)

10-1 Practice purpose

10-2 Practice items

10-2-1 0 to 59 second Timer

10-2-2 PILI LED circuit

10-2-3 Advertisement LED

Appendix A: Data Manual

Total 144 pages updated on Aug 27, 2003